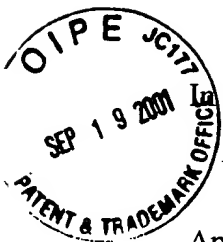


740756-1638



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Large PATENT application of:

Hisashi OHTANI et al.

Application No.: 08/807,737

Filed: February 27, 1997

For: METHOD FOR MANUFACTURING
SEMICONDUCTOR DEVICE

) Art Unit: 2813

) Examiner: Evan PERT

#42/IDS
9/25/01
V. Vannal

SUPPLEMENTAL
INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, DC 20231

September 19, 2001

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the reference listed on the attached Form PTO-1449 be made of record in the above-identified application. To assist the Examiner, the document is listed on the attached Form PTO-1449. It is respectfully requested that an Examiner-initialed copy of this form be returned to the undersigned.

As U.S. Patent No. 5,646,424 is the U.S. counterpart of JP 6-267978 no fee is required with this filing.

The Commissioner is hereby authorized to charge any fees connected with this filing which may be required now, or credit any overpayment to Deposit Account No. 19-2380.

Respectfully submitted,

By: _____

Luan C. Do
Reg. No. 38,434

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TECHNOLOGY CENTER 2800

NIXON PEABODY LLP
8180 Greensboro Drive, Suite 800
McLean, Virginia 22102